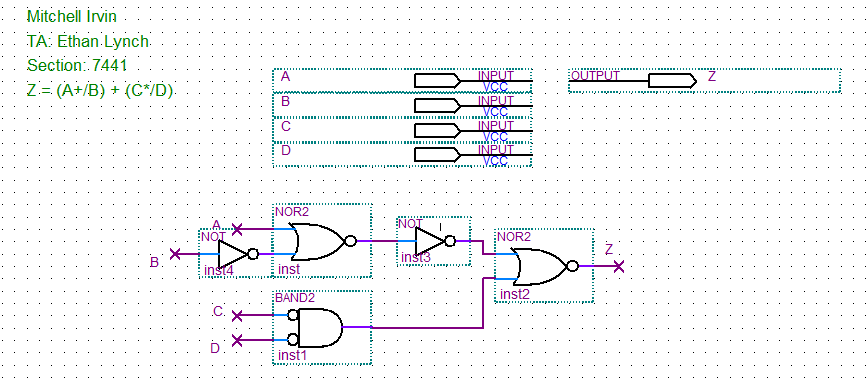
Lab 2

EEL 3701c

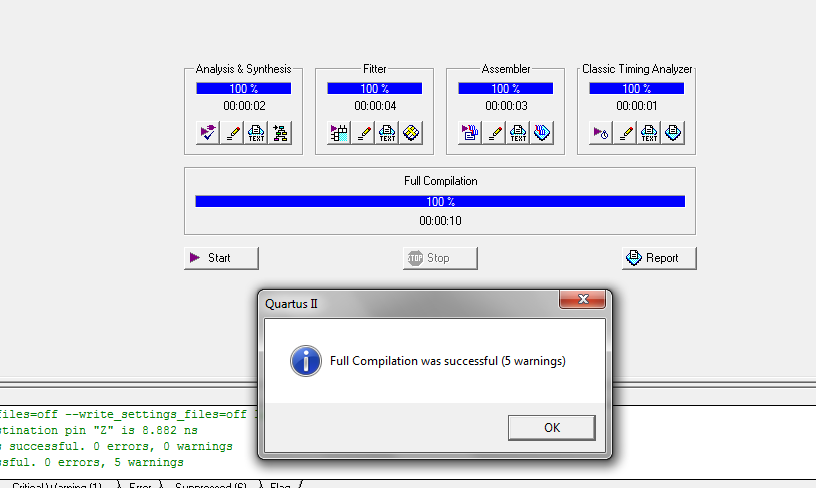
Mitchell Irvin

Part 1:

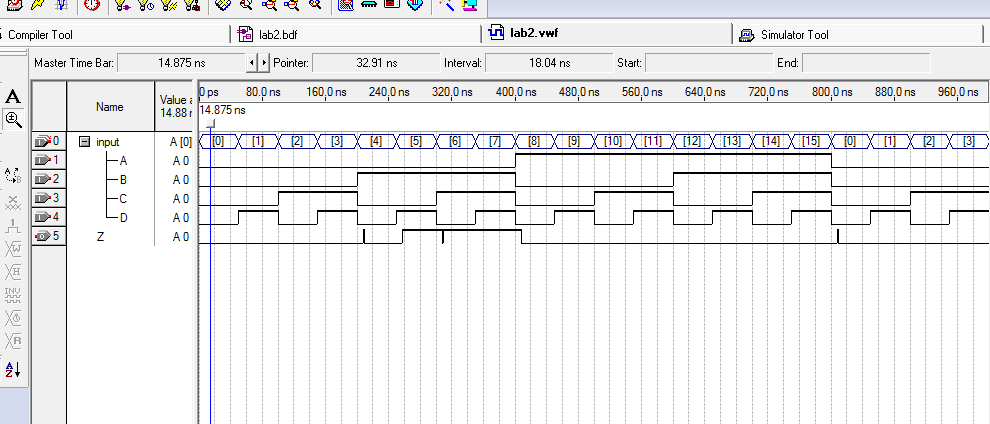
Block Diagram



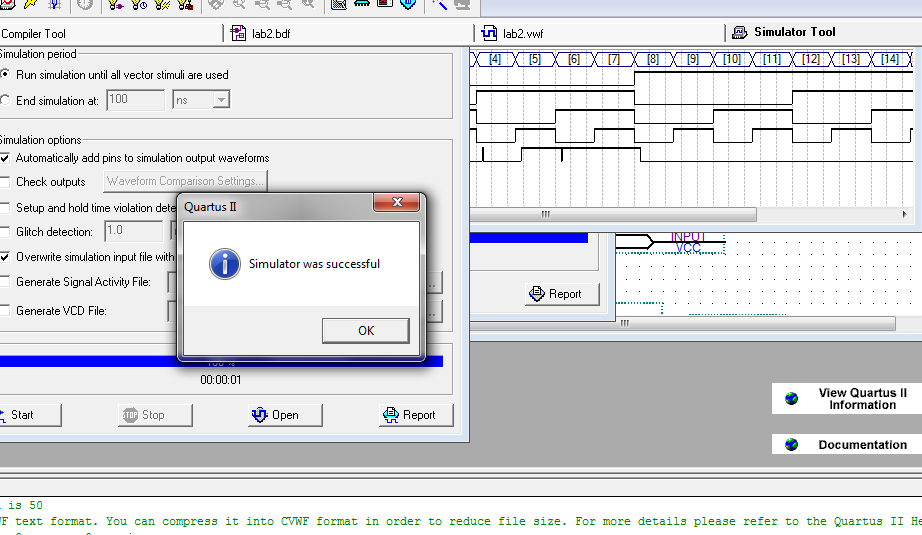
Compilation Output:



Waveform file

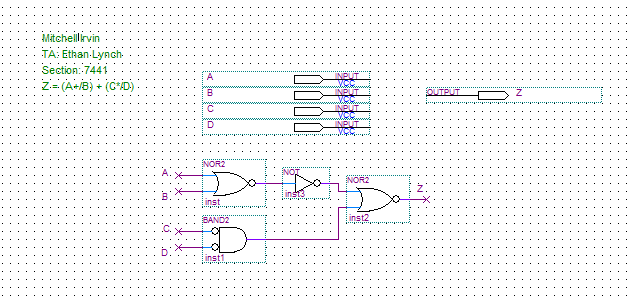


Waveform Simulation output:

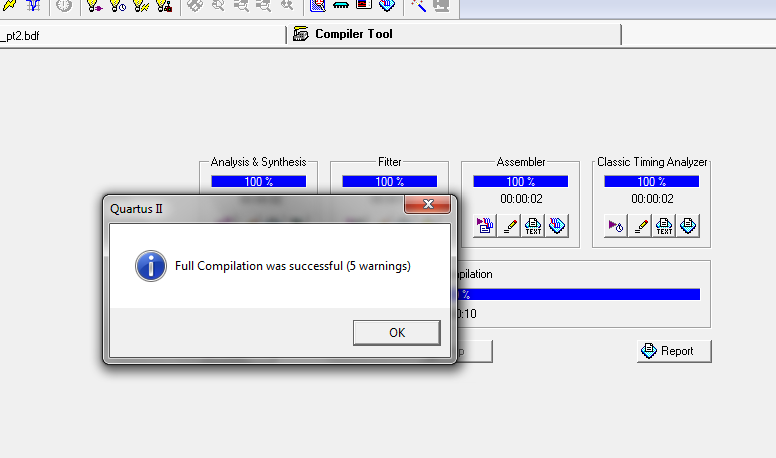


PART 2:

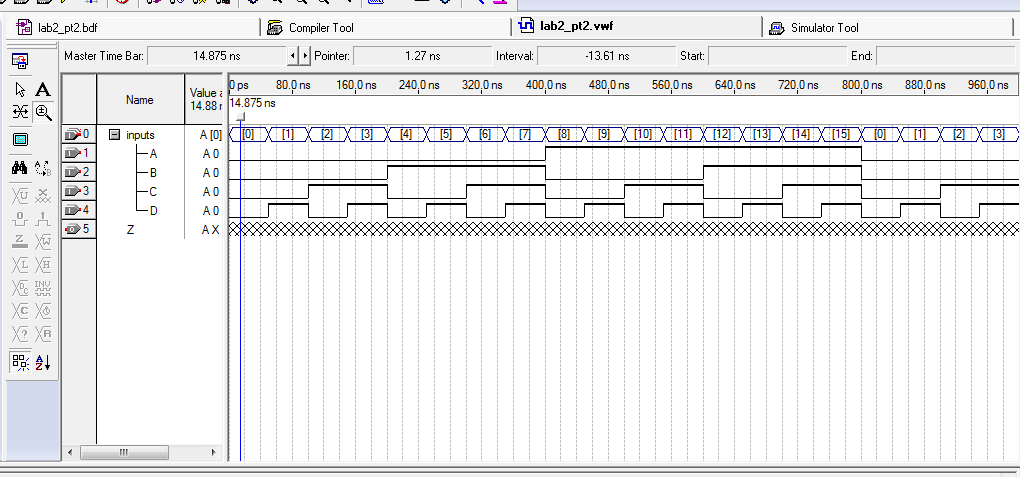
Block Diagram



Block Diagram Compilation Output:



Waveform File for part 2:



Waveform Simulation Output:

